

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Parruck, et al.

Assignee: Azanda Network Devices

Title: "Multi-Service Segmentation and Reassembly Device Involving Multiple Data Path Integrated Circuits"

Serial No.: 09/976,504

Filed: October 12, 2001

Examiner: Unknown

Group Art Unit: 2661

Atty. Doc. No.: AZA-003-6D/2001-P009

COPY OF PAPERS  
ORIGINALLY FILED

RECEIVED

MAR 04 2002

Technology Center 2600  
February 4, 2002

ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

SUPPLEMENTAL PRELIMINARY AMENDMENT

Sir:

Before action on the merits, please amend the above-identified application as follows.

IN THE SPECIFICATION:

Page 1, please delete the "Cross-Reference To Related Application" section, and please insert the following:

CROSS-REFERENCE TO RELATED APPLICATIONS

This application claims priority under 35 U.S.C. §120 from U.S. Patent Application Serial Number 09/851,565, filed May 8, 2001. This application also claims priority under 35 U.S.C. §120 from U.S. Patent Application Serial Number 09/823,667, filed March 30, 2001. This application also claims priority under 35 U.S.C. §120 from U.S. Application Serial Number 09/779,381, filed February 7, 2001.

Gp/2661  
4/B  
BA3/6/02



B1